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02/03/2004

WOLF GREENFIELD & SACKS, PC
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BOSTON, MA 02210-2211

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GAIL DAISCOLL	(Depositor's name)
Gail Daiscoll	(Signature)
5-3-04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/925,080	08/08/2001	Ferruccio Frisina	S1022/8707	3073

TITLE OF INVENTION: METHOD OF MANUFACTURING AN INTEGRATED EDGE STRUCTURE FOR HIGH VOLTAGE SEMICONDUCTOR DEVICES, AND RELATED INTEGRATED EDGE STRUCTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	05/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LUU, CHUONG A	2825	257-355000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

- 1 Lisa K. Jorgenson
- 2 James H. Morris
- 3 Wolf, Greenfield & Sacks, P.C.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

STMicroelectronics S.r.l.

Agrate Brianza, Italy

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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- ☒ Issue Fee
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- ☒ Advance Order - # of Copies 10

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(Authorized Signature) 	(Date) 4/5/04
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01 FC:1501
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TRANSMIT THIS FORM WITH FEE(S)



ATTORNEY'S DOCKET NO.: S1022.80707US00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ferruccio FRISINA
Serial No.: 09/925,080
Filed: August 8, 2001
For: METHOD OF MANUFACTURING AN INTEGRATED EDGE STRUCTURE
FOR HIGH VOLTAGE SEMICONDUCTOR DEVICES, AND RELATED
INTEGRATED EDGE STRUCTURE

Examiner: Chuong A. Luu
Art Unit: 2825

Confirmation No.: 3073

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Transmitted herewith for filing is/are the following document(s):

☒ Issue Fee Transmittal
☒ Return Postcard

If the enclosed papers are considered incomplete, the Mail Room and/or the Application Branch is respectfully requested to contact the undersigned collect at (617)720-3500, Boston, Massachusetts.

A check in the amount of \$1,660.00 is enclosed to cover the \$1,330.00 issue fee, \$300.00 publication fee and \$30.00 for soft copies. If the fee is insufficient, the balance may be charged to the account of the undersigned, Deposit Account No. 23/2825. A duplicate of this sheet is enclosed.

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Attorney Docket No.: S1022.80707US00
X05/03/04

Respectfully submitted,

Ferruccio Frisina, Applicant

By:

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